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STATEMENT DI ALI EIOANI				Art Unit	Unknown	
(us	e as many sh	eets as ne	cessary)	Examiner Name	Unknown	
Sheet	1	of	1	Attorney Docket Number	X-1396 US	

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		HALTER, JONATHAN P., "A Gate-Level Leakage Power Red CMOS Circuits," IEEE Custom Integrated Circuits Conference Avenue, 17th Floor, New York, NY., 10016-5997.	eduction Method for Ultra-Low-Power ice, 1997, pages 475-478, IEEE, 3 Park			
		YEAP, GARY, "Practical Low Power Digital VLSI Design," 19 Academic Publishers, 101 Philip Drive, Assinippi Park, Norw	998, pages 59-72, available from Kluwer well, Massachusetts, 02061			
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